

FIG. 1

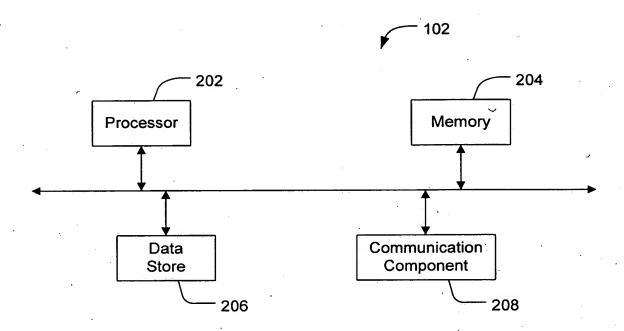


FIG. 2

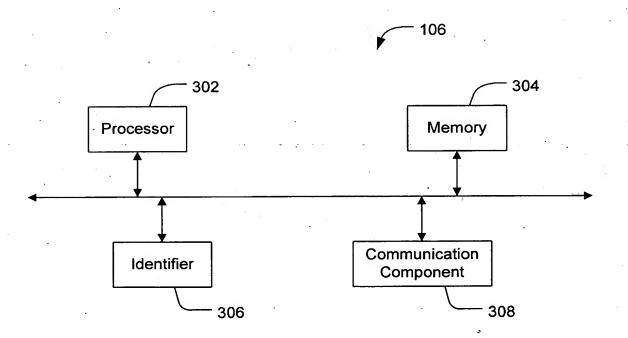
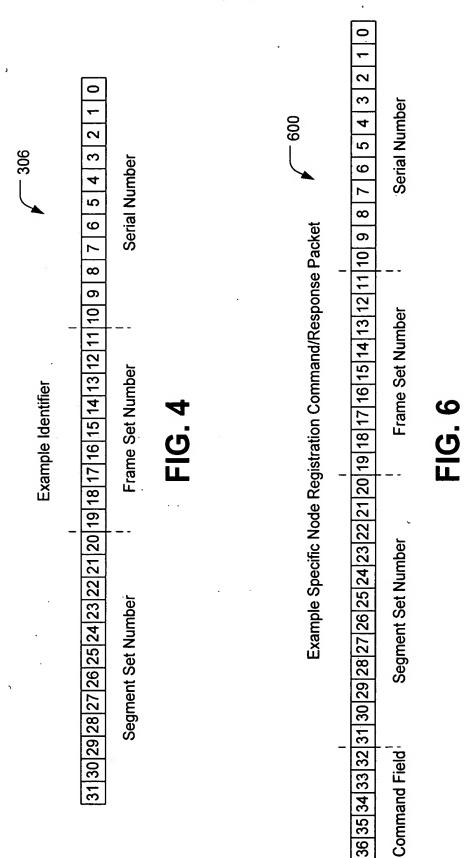


FIG. 3



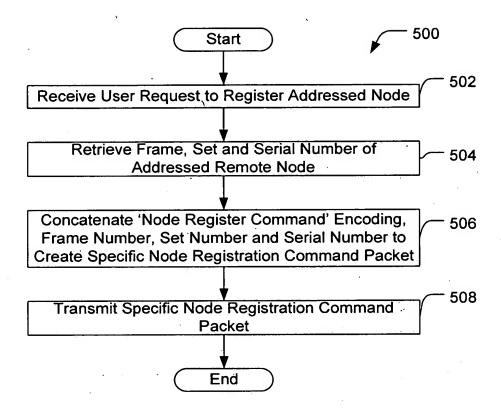
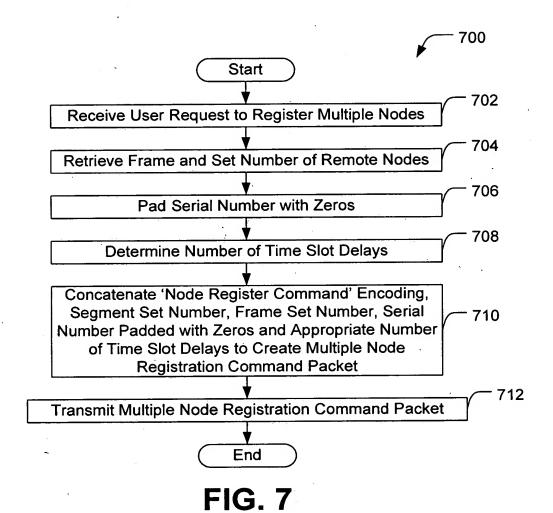


FIG. 5

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Time Slot

Interrogator Node
Calibration Period
Response Node
Transmission
Period

FIG. 8

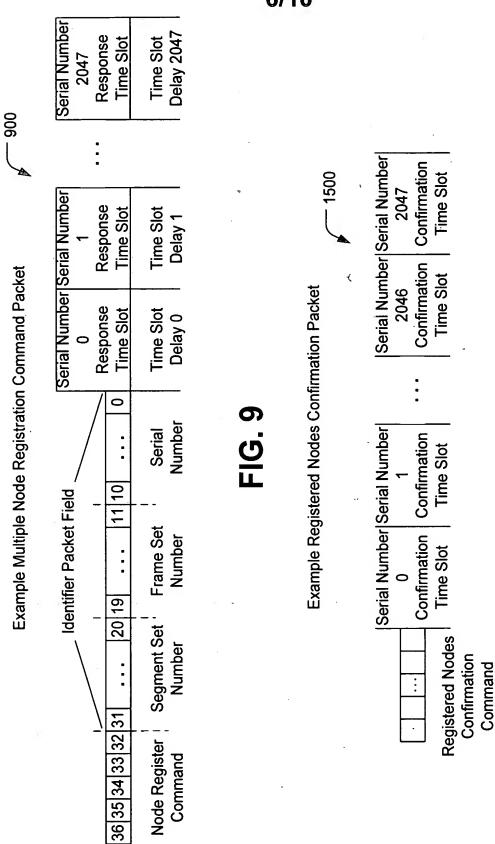
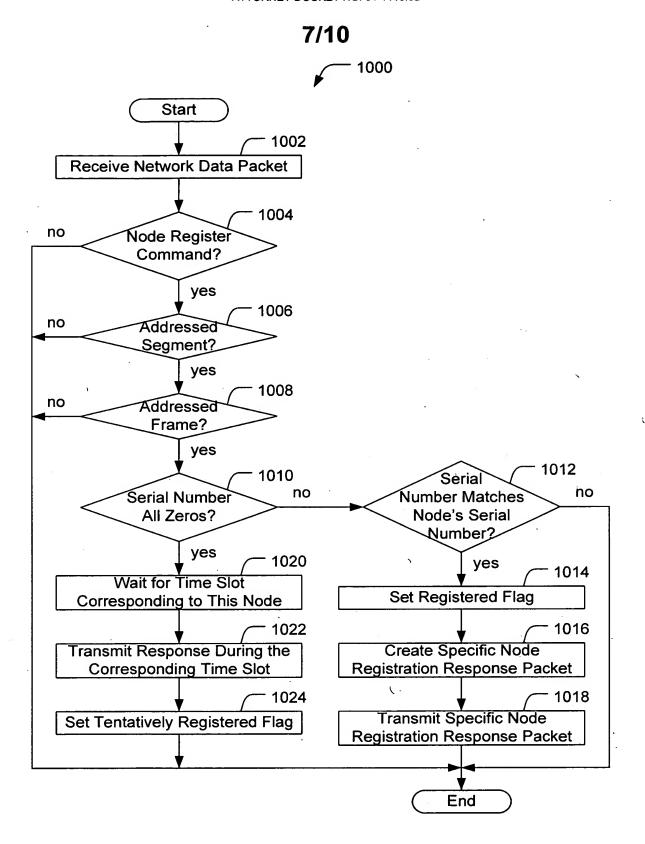
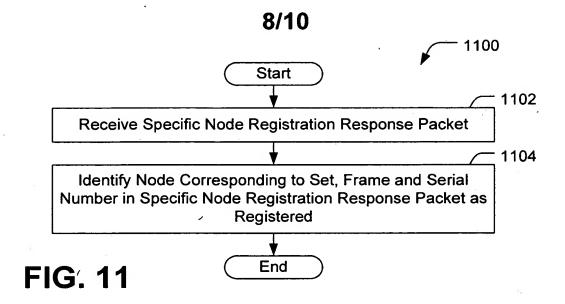
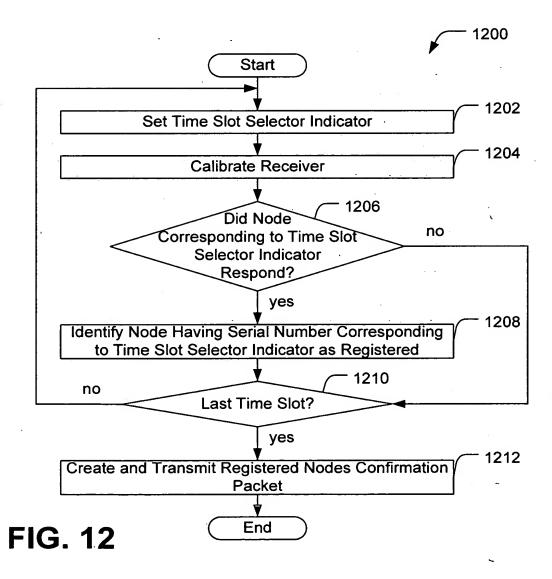


FIG. 15



**FIG. 10** 





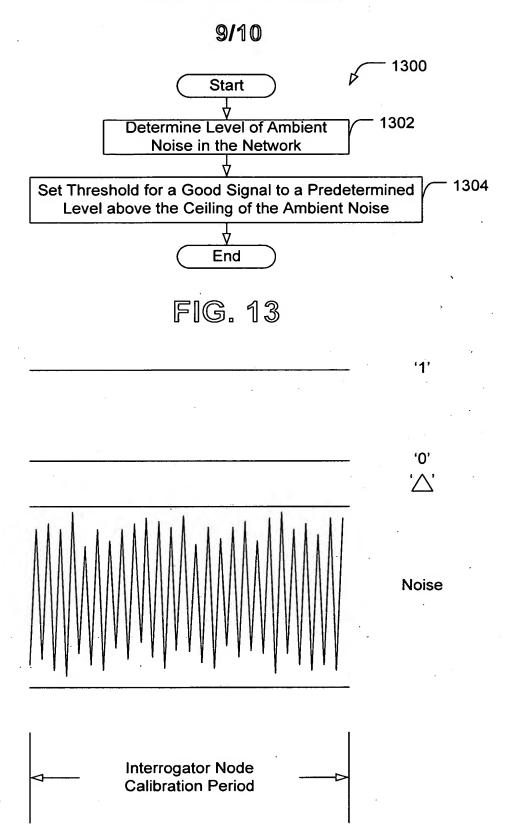


FIG. 14

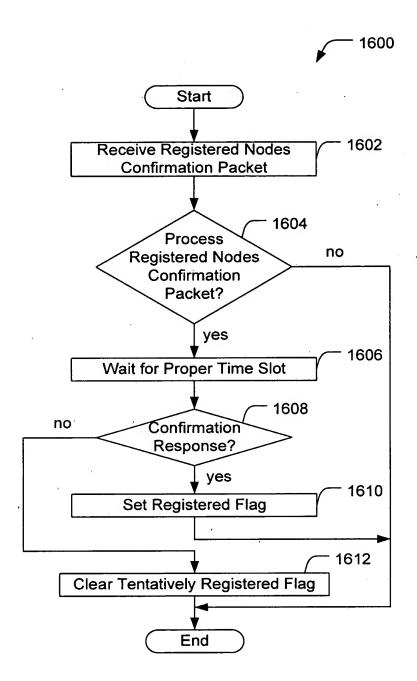


FIG. 16